Abstracts

Practical metal loss implementation for a microstrip line structure using SIBC in FDTD simulation

E. Takagi, B. Houshmand and T. Itoh. "Practical metal loss implementation for a microstrip line structure using SIBC in FDTD simulation." 1997 MTT-S International Microwave Symposium Digest 3. (1997 Vol. III [MWSYM]): 1531-1534.

Metal transmission loss of a microstrip line was calculated by FDTD simulation using surface impedance boundary condition (SIBC) technique. Calculated results were compared to measurement results. Proper SIBC implementation in FDTD simulation produced good results, even if coarse grids were used for a microstrip line structure.

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